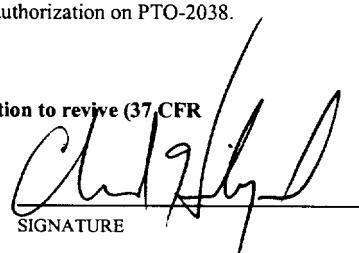


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FORM PTO-1390 (REV. 01-2000)		U S DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY 'S DOCKET NUMBER
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371				U.S. APPLICATION NO. (If known, see 37 CFR 1.5) <b>09/913840</b>
INTERNATIONAL APPLICATION NO. PCT/GB00/00174	INTERNATIONAL FILING DATE 24 January 2000	PRIORITY DATE CLAIMED 22 January 1999		
TITLE OF INVENTION OFDM RECEIVER WITH ADAPTIVE EQUALISER				
APPLICANT(S) FOR DO/EO/US Simon ARMOUR, David Roger BULL, and Andrew Robert NIX				
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:				
<p>1. <input checked="" type="checkbox"/> This is a <b>FIRST</b> submission of items concerning a filing under 35 U.S.C. 371.</p> <p>2. <input type="checkbox"/> This is a <b>SECOND</b> or <b>SUBSEQUENT</b> submission of items concerning a filing under 36 U.S.C. 371.</p> <p>3. <input type="checkbox"/> This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (21) indicated below.</p> <p>4. <input type="checkbox"/> The US has been elected by the expiration of 19 months from the priority date (Article 31).</p> <p>5. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371(c)(2))</p> <ul style="list-style-type: none"><li>a. <input checked="" type="checkbox"/> is attached hereto (required only if not communicated by the International Bureau).</li><li>b. <input type="checkbox"/> has been communicated by the International Bureau</li><li>c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US).</li></ul> <p>6. <input type="checkbox"/> An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)).</p> <ul style="list-style-type: none"><li>a. <input type="checkbox"/> is attached hereto.</li><li>b. <input type="checkbox"/> has been previously submitted under 35 U.S.C. 154(d)(4).</li></ul> <p>7. <input type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3)).</p> <ul style="list-style-type: none"><li>a. <input type="checkbox"/> are attached hereto (required only if not communicated by the International Bureau).</li><li>b. <input type="checkbox"/> have been communicated by the International Bureau.</li><li>c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired.</li><li>d. <input type="checkbox"/> have not been made and will not be made.</li></ul> <p>8. <input type="checkbox"/> An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).</p> <p>9. <input type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).</p> <p>10. <input type="checkbox"/> An English language translation of the annexes of the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).</p>				
<b>Items 11 to 20 below concern document(s) or information included:</b>				
<p>11. <input type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98.</p> <p>12. <input type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.</p> <p>13. <input type="checkbox"/> A <b>FIRST</b> preliminary amendment.</p> <p>14. <input type="checkbox"/> A <b>SECOND</b> or <b>SUBSEQUENT</b> preliminary amendment.</p> <p>15. <input type="checkbox"/> A substitute specification.</p> <p>16. <input type="checkbox"/> A change of power of attorney and/or address letter.</p> <p>17. <input type="checkbox"/> A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 – 1.825.</p> <p>18. <input type="checkbox"/> A second copy of the published international application under 36 U.S.C.</p> <p>19. <input type="checkbox"/> A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).</p> <p>20. <input type="checkbox"/> Other items or information:</p>				

I/S Application no. (if known, see 37 CFR 1.5) <b>09/913840</b>	INTERNATIONAL APPLICATION NO.	ATTORNEY'S DOCKET NUMBER
21. <input type="checkbox"/> The following fees are submitted:		CALCULATIONS PTO USE ONLY
<b>BASIC NATIONAL FEE (37 CFR 1.492(A) (1) – (5)):</b>		
Neither international preliminary examination fee (37 CFR 1.492) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO .....		<b>\$1000.00</b>
International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search report prepared by the EPO of JPO .....		<b>\$860.00</b>
International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO .....		<b>\$710.00</b>
International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4) .....		<b>\$690.00</b>
International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)(4) .....		<b>\$100.00</b>
<b>ENTER APPROPRIATE BASIC FEE AMOUNT = 860.00</b>		
Surcharge of <b>\$130.00</b> for furnishing the oath or declaration later than <input type="checkbox"/> 20 <input checked="" type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(e)).		<b>\$130.00</b>
CLAIMS	NUMBER FILED	NUMBER EXTRA
Total claims	9 - 20 =	x <b>\$18.00</b>
Independent claims	3 - 3 =	x <b>\$80.00</b>
<b>MULTIPLE DEPENDENT CLAIM(S) (if applicable)</b>		+ <b>270.00</b>
<b>TOTAL OF ABOVE CALCULATIONS =</b>		<b>\$990.00</b>
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27. The fees indicated above are reduced by 1/2.		+ <b>\$</b>
<b>SUBTOTAL =</b>		<b>\$990.00</b>
Processing fee of <b>\$130.00</b> for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(f)).		<b>\$</b>
<b>TOTAL NATIONAL FEE =</b>		<b>990.00</b>
Fee for recording the enclosed assignment (37 CFR 1.2(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). <b>\$40.00</b> per property		+ <b>\$</b>
<b>TOTAL FEES ENCLOSED =</b>		<b>\$990.00</b>
		<b>Amount to be refunded:</b> <b>\$</b>
		<b>charged:</b> <b>\$</b>
<p>a. <input type="checkbox"/> A check in the amount of \$_____ to cover the above fees is enclosed.</p> <p>b. <input checked="" type="checkbox"/> Please charge my Deposit Account No. <u>20-1430</u> in the amount of <u>\$990.00</u> to cover the above fees.</p> <p>c. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. <u>20-1430</u>. A duplicate copy of this sheet is enclosed.</p> <p>d. <input type="checkbox"/> Fees are to be charged to a credit card. <b>WARNING:</b> Information on this form may become public. <b>Credit card information should not be included on this form.</b> Provide credit card information and authorization on PTO-2038.</p>		
<p><b>NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b) must be filed and granted to restore the application to pending status.</b></p> <p>SEND ALL CORRESPONDENCE TO:</p> <p>Two Embarcadero Center Eighth Floor San Francisco, CA 94111-3834</p> <p> SIGNATURE</p> <p>Chad S. Hilyard NAME</p> <p>40,647 _____ REGISTRATION NUMBER</p>		

OFDM RECEIVER WITH ADAPTIVE EQUALISER

5 This invention relates to a receiver, and in particular to an OFDM receiver, and to an adaptive equalizer for use in such a receiver and to a method of training the equalizer.

10 The European digital terrestrial television standard, DVB-T, specifies orthogonal frequency division multiplexing, OFDM as the modulation scheme.

15 In conventional OFDM systems, multipath radio transmission channels cause delay spread distortion of the transmitted signal. Although adaptive equalizers can be used in many applications to cancel out delay spread distortion, existing designs for OFDM receivers and adaptive equalizers are generally incompatible, although it has been proposed to use a number of equalizers acting in parallel on respective sub-bands of the OFDM signal.

20 Therefore, existing designs for OFDM systems employ guard intervals to combat the effects of delay spread. Each transmission period includes a period during which useful information is transmitted, and a guard interval, during which no useful information is transmitted. Thus, use of a guard interval reduces 25 efficiency of the transmission, and less data can be transmitted within a given radio frequency bandwidth.

30 The present invention relates to an OFDM receiver including an adaptive equalizer, which attempts to overcome the incompatibility of existing designs of OFDM receivers and adaptive equalizers.

According to a first aspect of the present invention, there is therefore provided a receiver as defined in claim 1.

35 According to a second aspect of the present invention, there is therefore provided an equalizer as defined in claim 6.

According to a third aspect of the present invention, there is therefore provided a method as defined in claim 7.

For a better understanding of the present invention, reference will now be made to the accompanying drawings, in which:-

Figure 1 shows a first receiver in accordance with the invention;

Figure 2 shows a first equalizer in accordance with the invention;

Figure 3 shows a second equalizer in accordance with the invention;

Figure 4 shows a second receiver in accordance with the invention.

#### DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

As Shown in Figure 1, a transmitted analog OFDM symbol is received over a radio frequency channel 2 having unknown and time-varying characteristics, and is subject to the addition of noise.

The receiver therefore takes an input signal  $y_1'(t)$  in an input device 4, where it is demodulated, A-D converted and down-sampled to give a time domain vector  $y'(n,1)$ . The received sequence  $y'(n,1)$  is filtered by an adaptive equalizer 6 to produce a time-domain sequence  $z'(n,1)$ .

The time domain sequence output from the equalizing filter 6 is input to a guard extraction device 8 to form an output  $z(n,1)$ . An advantage of the present invention is that the need for a guard interval may be reduced or eliminated. The guard extraction device 8 is therefore required only if it is needed if a guard interval is used by the transmitter which is in use.

A Fast Fourier Transform (FFT) 10 is then applied to  $z(n,1)$  to produce a frequency domain vector  $Z(k,1)$ .

In this embodiment of the invention, the frequency

domain vector is supplied to a channel estimator 12 which derives an estimation of the radio channel's frequency response and compensates according to this estimate. In an alternative embodiment, the channel estimator may be omitted.

The vector is then supplied to an output device 14 to produce a data output. The output device 14 may be nothing more than a hard limiter, putting the data into a useable form. However, any interleaving, coding, signal mapping or other error control strategies applied in the transmitter are reversed in the output device 14 and exploited to correct any errors and produce the output data sequence. The output data, which is the best possible estimate of the data content of the transmitted signal is then applied to a feedback loop, and processed to form the best possible estimate of the transmitted sequence. Specifically, the data output is applied to a device 16 which interleaves, codes and maps the data in an identical manner to that performed at the transmitter to produce the frequency domain sequence  $W(k,l)$ .

The frequency domain sequence is input to an inverse Fourier transform device 18 and the output  $w(n,l)$  of this device is applied to a guard interval insertion device 20 to insert a guard interval corresponding to any that was added in the transmitter to generate a sequence  $w'(n,l)$ . In effect, the data output is processed in the feedback loop to effectively attempt to reconstruct the OFDM symbol that was generated at the transmitter, and hence  $w'(n,l)$  is an estimate of the transmitted sequence  $x'(n,l)$ .

The estimate  $w'(n,l)$  is then input to the equalizing filter 6 to enable decision directed adaptation of the tap coefficients thereof.

Each updated estimate of an OFDM symbol, made up of a number of sub-symbols, is available to the

equalizer only after all sub-symbols of one OFDM symbol have been received.

Figure 2 shows the equalizer of the present invention. As is generally conventional, the equalizer consists of two shift register sections: a feedforward section 32 that stores future transmission symbols and the current transmission symbol and a feedback section 34 that stores previous transmission symbols. The length of the feedforward section is  $(J_1+1)$  symbols and the length of the feedback section is  $J_2$  symbols. The received sequence  $y'(n,1)$  is input, the value of each stored symbol is multiplied by a tap coefficient  $c(j)$ , and the results are summed to form the equalizer output  $z'(n,1)$ .

The tap coefficients  $c(j)$  are updated according to the LMS adaptation algorithm. As is known, the LMS algorithm adapts the equalizer taps according to the error measure between the equalizer output symbols and an estimate of the transmitted symbols. The estimate of the transmitted symbols can be achieved either by means of a training sequence or by decision directed adaptation.

Adaptation according to a training sequence requires the transmission of symbols of which the equalizer has prior knowledge. This training mode allows adaptation according to an error free estimate of transmitted symbols but results in no data throughput.

Decision directed adaptation feeds back output data to estimate the transmitted symbols. This estimate is not necessarily error free but does allow for data throughput.

In practice, the two techniques are combined. An initial training sequence approximately adapts the equalizer and reduces equalizer output error sufficiently that decision directed adaptation may

5

provide an adequate error measurement for further adaptation. During decision directed adaptation the equalizer further adapts both to 'fine tune' its action and to track any relatively small time variation in the radio channel.

The equalizer of Figure 2 is generally similar to a conventional equalizer, although there are differences as follows:-

10 Symbols input to the feedback register 32 during decision directed operation do not come directly from the decision device but rather from the feedback vector.

15 Symbols used to calculate error during decision directed operation come similarly from the feedback vector rather than direct from the decision device.

20 Additional buffers 36, 38 for both feedforward and feedback sections further store past symbols beyond the duration of the feedforward/feedback sections. These additional stored values are required for decision directed adaptation

25 During decision directed mode the tap coefficients are not updated at intervals of the sub-symbol period, but rather at the OFDM symbol period, regarding one OFDM symbol as made up a group of sub-symbols.

30 However, the LMS calculation is performed in the LMS calculation processor 40 to calculate adjustments corresponding to intervals of the sub-symbol period with the sum of all calculated adjustments being made at intervals of the OFDM symbol period.

35 The equalizer output may be directly input to the feedback section thereof.

These additional features, which apply during a decision directed (or data derived) operation, enable the equalizer to operate within the OFDM receiver architecture.

More detailed explanation of all these features is

given below.

Since the equalizer operates on the received time domain sequence its output is also a time domain sequence. Data in the OFDM modem is in the frequency domain and hence the equalizer output must be Fourier Transformed (with any guard interval first being removed) prior to the decision process. Thus, the output of the equalizer cannot be applied directly to the decision device. Similarly the decision device output cannot be fed back directly to the equalizer. The feedback vector is fed back instead. If the equalizer is adapting according to a training sequence this is irrelevant, since the training sequence itself is an error free estimate of the transmitted sequence and can be input to the equalizer's feedback section. Furthermore, the decision device output cannot be used to calculate the output error used in the LMS adaptation algorithm and the feedback vector is again used instead.

The additional symbol buffering and the changes to the adaptation process in decision directed mode are necessary since the parallel transmission undertaken in OFDM results in the group post-decision data sub-symbols making up one OFDM symbol being simultaneously output at intervals of the OFDM symbol period. (By contrast, a single carrier system outputs individual symbols at intervals of the symbol period.) This restriction imposed by the parallel transmission prevents decision directed adaptation during OFDM symbols. It also prevents feedback values becoming available to the equalizer until the end of the OFDM symbol.

Therefore, instead of adapting the equalizer with each transmission sub-symbol, all transmission symbols forming the OFDM symbol are clocked through the equalizer with the tap coefficients remaining constant

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and the equalizer output is input directly into the feedback section. At the end of each OFDM symbol the past and future transmission symbols that were stored in the feedforward and feedback sections at the start 5 of the OFDM symbol are recovered from the buffers. Since the tap coefficients remain constant throughout the OFDM symbol the equalizer is effectively returned to its state at the start of the OFDM symbol. The OFDM symbol is then clocked into the equalizer once more 10 with the feedback OFDM symbol now available both for determination of error and input to the feedback section. This process enables the adaptation steps that had not previously taken place to be completed and replaces the fed back output symbols with post decision 15 estimates of the transmitted symbols as provided by the feedback OFDM symbol.

This method of operation described above allows both the feedforward and feedback stages of the 20 equalizer to have numbers of taps which are integer multiples of the length of the extended OFDM symbol, which is considered to be  $(N+M)$ , with  $N$  the number of transmission symbols in the useful period of an OFDM symbol, and  $M$  the number of transmission symbols in the guard period. In this illustrated embodiment, these 25 numbers of taps are equal to the length of the extended OFDM symbol.

The equalizer output is then given by:-

$$z'(n, l) = \sum_{j=-J_1}^{-J_1+n} c(j) y'((n - j - (N + M)), l + 1) + \sum_{j=J_1+n+1}^0 c(j) y'((n - j), l) \\ + C_{out}(n) \sum_{j=1}^n c(j) z'((n - j), l) + \sum_{j=n+1}^{J_2} c(j) w'((n + (N + M) - j), l - 1)$$

where:

$$C_{out}(n) = 0 \quad \text{for} \quad n = 0$$

and:

$$C_{out}(n) = 1 \quad \text{for} \quad n \neq 0$$

The equalizer is then adapted according to the LMS algorithm, which in this case is defined as follows:-

**During Training Mode:**

$$c(j, n+1, l) = c(j, n, l) + \Delta \varepsilon'(n, l) y^* ((n - j - (N + M)), l + 1)$$

for  $-J_1 \leq j \leq -J_1 + n + 1$

$$c(j, n+1, l) = c(j, n, l) + \Delta \varepsilon'(n, l) y^* ((n - j), l)$$

for  $-J_1 + n + 2 \leq j \leq 0$

$$c(j, n+1, l) = c(j, n, l) + \Delta \varepsilon'(n, l) x^* ((n - j), l)$$

for  $1 \leq j \leq n$

$$c(j, n+1, l) = c(j, n, l) + \Delta \varepsilon'(n, l) x^* ((n + (N + M) - j), l - 1)$$

for  $n + 1 \leq j \leq J_2$

$$\varepsilon'(n, l) = x'(n, l) - z'(n, l)$$

**During Decision Directed Operation**

$$c(j, n, l + 1) = c(j, n, l) + C_F(j) \sum_{n=0}^{N+M+j-1} \Delta \varepsilon'(n, l) y^* ((n - j), l) + \sum_{n=N+M+j}^{N+M-1} \Delta \varepsilon'(n, l) y^* ((n - (N + M) - j), l + 1)$$

for  $-J_1 \leq j \leq 0$

$$c(j, n, l + 1) = c(j, n, l) + \sum_{n=0}^{j-1} \Delta \varepsilon'(n, l) w^* ((n + (N + M) - j), l - 1) + C_B(j) \sum_{n=j}^{N+M-1} \Delta \varepsilon'(n, l) w^* ((n - j), l)$$

for  $1 \leq j \leq J_2$

$$\varepsilon'(n, l) = w'(n, l) - z'(n, l)$$

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where:

 $C_f(j) = 0$  for  $-j = N + M$ 

and:

 $C_f(j) = 1$  for  $-j \neq N + M$ 

5

 $C_f(j) = 0$  for  $j = N + M$ 

and:

 $C_f(j) = 1$  for  $j \neq N + M$ 

10

 $\Delta$  Equalizer step constant $c(j, n, l)$  Equalizer tap vector in OFDM system $\epsilon(n, l)$  Equalizer output error vector in OFDM system

15

Figure 3 shows an alternative equalizer in accordance with the invention. It corresponds generally to that of Figure 2, except that the symbols input to the equalizer's feedback section 34 are zero symbols. This prevents the feedback of noise bearing symbols into the equalizer but, as a consequence, the equalizer no longer cancels delay spread distortion from within the same OFDM symbol. However this will still allow for an effective transmission strategy since, for example, where a guard interval is employed, delay spread interference within the same OFDM symbol does not cause inter-carrier interference (ICI). Thus, an equalizer with zero symbol feedback may be combined with a channel estimation process to achieve cancellation of delay spread distortion without the need to feed back pre-decision transmission symbols.

20

Thus, in Figure 3, compared with Figure 2, the feedback path from the equalizer output directly to the feedback section is replaced by a zero symbol source.

25

The equalizer output then becomes:-

30

$C_{out}(n) = 0$ , for all  $n$ .

35

The method of tap adaptation in the equalizer of figure 3 is the same as that described above with reference to Figure 2.

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The equalizer is no longer required to cancel all delay spread distortion and hence the equalizer output is not expected to approximate the transmitted sequence. The receiver is therefore as shown in Figure 4, as the feedback vector must be adjusted to compensate the uncombatted distortion. This is achieved by reversing the channel compensation process in the feedback path. Thus the channel estimate produced by the channel estimator 12 is applied in reverse to the output of mapping and interleaving device 16. This achieves a feedback vector which is suitably adjusted.

There is thus described a receiver, and an equalizer, which allow adaptive equalization of OFDM signals, with a consequent improvement in the noise in the received signals.

CLAIMS

1. An OFDM receiver, comprising:
  - a received signal processor, for demodulating, A-D converting, and down-sampling received signals to form a time domain digital signal vector;
  - an equalizer, having at least a feedback section and having a plurality of tap coefficients, the equalizer acting on the time domain digital signal vector to form a filtered time domain sequence;
  - a Fast Fourier Transform processor, for acting on the filtered time domain sequence to form a frequency domain vector;
  - an output device, for converting the frequency domain vector into an output data signal;
  - an inverse Fast Fourier Transform processor, for acting on the output data signal to form an estimate of the transmitted sequence;
  - wherein the estimate of the transmitted sequence is supplied as an input to the equalizer to enable decision directed adaptation of the tap coefficients thereof, and further as an input to the feedback section of the equalizer.
2. An OFDM receiver as claimed in claim 1, further comprising:
  - a guard extraction device, for extracting any guard sequence from the filtered time domain sequence before processing by the Fast Fourier Transform processor; and
  - a guard insertion device, for inserting a guard interval, corresponding to that removed by the guard extraction device, into the estimate of the transmitted sequence supplied by the inverse Fast Fourier Transform processor.
3. An OFDM receiver as claimed in claim 1, further comprising:

a channel estimator, for compensating the frequency domain vector to produce a compensated frequency domain vector.

4. An OFDM receiver as claimed in claim 1, wherein the output device comprises means for exploiting and cancelling any error control strategies applied in a transmitter of the received signals, and further comprising:

means for reapplying to the output data signal any error control strategies cancelled in the output device.

5. An OFDM receiver as claimed in claim 4, wherein the output device comprises means for deinterleaving the compensated frequency domain vector.

6. An OFDM receiver as claimed in claim 4, wherein the output device comprises means for demapping the compensated frequency domain vector.

7. An adaptive decision feedback equalizer, comprising:

20 a first input, for a received signal comprising a plurality of symbols, the symbols representing an OFDM signal, each OFDM symbol comprising a plurality of sub-symbols per OFDM symbol period;

25 a feedforward stage, comprising a first number of taps and corresponding tap coefficients;

a feedback stage, comprising a second number of taps and corresponding tap coefficients;

a first buffer, for storing past symbols beyond symbols present in the feedforward stage;

30 a second buffer, for storing past symbols beyond symbols present in the feedback stage;

35 a correction algorithm processor, for updating tap coefficients of taps in the feedback stage and the feedforward stage at intervals of the OFDM symbol period.

8. A method of adapting coefficients of an

adaptive equalizer, the equalizer comprising:

5 a first input, for a received signal comprising a plurality of symbols, the symbols representing an OFDM signal, each OFDM symbol comprising a plurality of sub-symbols per OFDM symbol period;

a second input, for receiving a feedback signal comprising an estimate of a transmitted signal;

10 a feedforward stage, comprising a first number of taps and corresponding tap coefficients;

a feedback stage, comprising a second number of taps and corresponding tap coefficients;

15 a first buffer, for storing past symbols beyond symbols present in the feedforward stage;

a second buffer, for storing past symbols beyond symbols present in the feedback stage; and

20 a correction algorithm processor, for updating tap coefficients of taps in the feedback stage and the feedforward stage at intervals of the OFDM symbol period, the method comprising:

supplying the feedback signal to the second input once per symbol period;

calculating required corrections to the tap coefficients corresponding to intervals of the sub-symbol period; and

25 updating the tap coefficients of the taps at intervals of the OFDM symbol period based on all calculated corrections obtained during each said period.

9. A method as claimed in claim 8, further comprising outputting an equalized signal at intervals of the symbol period.



## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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(71) Applicant (for all designated States except US): UNIVERSITY OF BRISTOL [GB/GB]; Senate House, Tyndall Avenue, Bristol BS8 1TH (GB).		Published With international search report.	
(72) Inventors; and			
(75) Inventors/Applicants (for US only): ARMOUR, Simon [GB/GB]; 689 Wellsway, Odd Down, Bath BA2 2TZ (GB). BULL, David, Roger [GB/GB]; Netherways, Netherhope Land, Tidenham, Chepstow, Monmouthshire NP16 7JE (GB). NIX, Andrew, Robert [GB/GB]; 7 Bellview, Clifton, Bristol BS8 1DA (GB).			
(74) Agent: O'CONNELL, David, Christopher; Haseltine Lake & Co., Imperial House, 15-19 Kingsway, London WC2B 6UD (GB).			
(54) Title: OFDM RECEIVER WITH ADAPTIVE EQUALISER			
<pre>     graph LR         X1["x1'(t)"] --&gt; CHANNEL[CHANNEL]         CHANNEL --&gt; SUM(( ))         NOISE["NOISE"] --&gt; SUM         SUM --&gt; Y1["y1'(t)"]         Y1 --&gt; EQUALIZER[EQUALIZER]         EQUALIZER --&gt; Z1["z1'(t)"]         Z1 --&gt; GUARD_EXTRACT[GUARD EXTRACTION]         GUARD_EXTRACT --&gt; Z2["z2(t)"]         Z2 --&gt; FFT[FFT]         FFT --&gt; Z3["z3(t)"]         Z3 --&gt; CHANNEL_ESTIMATOR[CHANNEL ESTIMATOR]         CHANNEL_ESTIMATOR --&gt; Z4["z4(t)"]         Z4 --&gt; DE_INTERLEAV[DE-INTERLEAVER, DE-MAPPING &amp; DECISION PROCESS]         DE_INTERLEAV --&gt; DATA_OUTPUT["DATA OUTPUT"]         DE_INTERLEAV --&gt; MAPPING["MAPPING &amp; INTERLEAVING"]         MAPPING --&gt; IFFT[IFFT]         IFFT --&gt; GUARD_INSERTION[GUARD INSERTION]         GUARD_INSERTION --&gt; Z5["z5(t)"]         Z5 --&gt; EQUALIZER         EQUALIZER --&gt; Z6["z6(t)"]         Z6 --&gt; GUARD_EXTRACT         Z6 --&gt; Z7["z7(t)"]         Z7 --&gt; FFT         Z7 --&gt; CHANNEL_ESTIMATOR         CHANNEL_ESTIMATOR --&gt; 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DE_INTERLEAV         DE_INTERLEAV --&gt; Z234["z234(t)"]         Z234 --&gt; IFFT         IFFT --&gt; GUARD_INSERTION         GUARD_INSERTION --&gt; Z235["z235(t)"]         Z235 --&gt; EQUALIZER         EQUALIZER --&gt; Z236["z236(t)"]         Z236 --&gt; GUARD_EXTRACT         Z236 --&gt; Z237["z237(t)"]         Z237 --&gt; FFT         Z237 --&gt; CHANNEL_ESTIMATOR         CHANNEL_ESTIMATOR --&gt; Z238["z238(t)"]         Z238 --&gt; DE_INTERLEAV         DE_INTERLEAV --&gt; Z239["z239(t)"]         Z239 --&gt; IFFT         IFFT --&gt; GUARD_INSERTION         GUARD_INSERTION --&gt; Z240["z240(t)"]         Z240 --&gt; EQUALIZER         EQUALIZER --&gt; Z241["z241(t)"]         Z241 --&gt; GUARD_EXTRACT         Z241 --&gt; Z242["z242(t)"]         Z242 --&gt; FFT         Z242 --&gt; CHANNEL_ESTIMATOR         CHANNEL_ESTIMATOR --&gt; Z243["z243(t)"]         Z243 --&gt; DE_INTERLEAV         DE_INTERLEAV --&gt; Z244["z244(t)"]         Z244 --&gt; IFFT         IFFT --&gt; GUARD_INSERTION         GUARD_INSERTION --&gt; Z245["z245(t)"]         Z245 --&gt; EQUALIZER         EQUALIZER --&gt; Z246["z246(t)"]         Z246 --&gt; GUARD_EXTRACT         Z246 --&gt; Z247["z247(t)"]         Z247 --&gt; FFT         Z247 --&gt; CHANNEL_ESTIMATOR         CHANNEL_ESTIMATOR --&gt; Z248["z248(t)"]         Z248 --&gt; DE_INTERLEAV         DE_INTERLEAV --&gt; Z249["z249(t)"]         Z249 --&gt; IFFT         IFFT --&gt; GUARD_INSERTION         GUARD_INSERTION --&gt; Z250["z250(t)"]         Z250 --&gt; EQUALIZER         EQUALIZER --&gt; Z251["z251(t)"]         Z251 --&gt; GUARD_EXTRACT         Z251 --&gt; Z252["z252(t)"]         Z252 --&gt; FFT         Z252 --&gt; CHANNEL_ESTIMATOR         CHANNEL_ESTIMATOR --&gt; Z253["z253(t)"]         Z253 --&gt; DE_INTERLEAV         DE_INTERLEAV --&gt; Z254["z254(t)"]         Z254 --&gt; IFFT         IFFT --&gt; GUARD_INSERTION         GUARD_INSERTION --&gt; Z255["z255(t)"]         Z255 --&gt; EQUALIZER         EQUALIZER --&gt; Z256["z256(t)"]         Z256 --&gt; GUARD_EXTRACT         Z256 --&gt; Z257["z257(t)"]         Z257 --&gt; FFT         Z257 --&gt; CHANNEL_ESTIMATOR         CHANNEL_ESTIMATOR --&gt; Z258["z258(t)"]         Z258 --&gt; DE_INTERLEAV         DE_INTERLEAV --&gt; Z259["z259(t)"]         Z259 --&gt; IFFT         IFFT --&gt; GUARD_INSERTION         GUARD_INSERTION --&gt; Z260["z260(t)"]         Z260 --&gt; EQUALIZER         EQUALIZER --&gt; Z261["z261(t)"]         Z261 --&gt; GUARD_EXTRACT         Z261 --&gt; Z262["z262(t)"]         Z262 --&gt; FFT         Z262 --&gt; CHANNEL_ESTIMATOR         CHANNEL_ESTIMATOR --&gt; Z263["z263(t)"]         Z263 --&gt; DE_INTERLEAV         DE_INTERLEAV --&gt; Z264["z264(t)"]         Z264 --&gt; IFFT         IFFT --&gt; GUARD_INSERTION         GUARD_INSERTION --&gt; Z265["z265(t)"]         Z265 --&gt; EQUALIZER         EQUALIZER --&gt; Z266["z266(t)"]         Z266 --&gt; GUARD_EXTRACT         Z266 --&gt; Z267["z267(t)"]         Z267 --&gt; FFT         Z267 --&gt; CHANNEL_ESTIMATOR         CHANNEL_ESTIMATOR --&gt; Z268["z268(t)"]         Z268 --&gt; DE_INTERLEAV         DE_INTERLEAV --&gt; Z269["z269(t)"]         Z269 --&gt; IFFT         IFFT --&gt; GUARD_INSERTION         GUARD_INSERTION --&gt; Z270["z270(t)"]         Z270 --&gt; EQUALIZER         EQUALIZER --&gt; Z271["z271(t)"]         Z271 --&gt; GUARD_EXTRACT         Z271 --&gt; Z272["z272(t)"]         Z272 --&gt; FFT         Z272 --&gt; CHANNEL_ESTIMATOR         CHANNEL_ESTIMATOR --&gt; Z273["z273(t)"]         Z273 --&amp;gt</pre>			

1 / 4

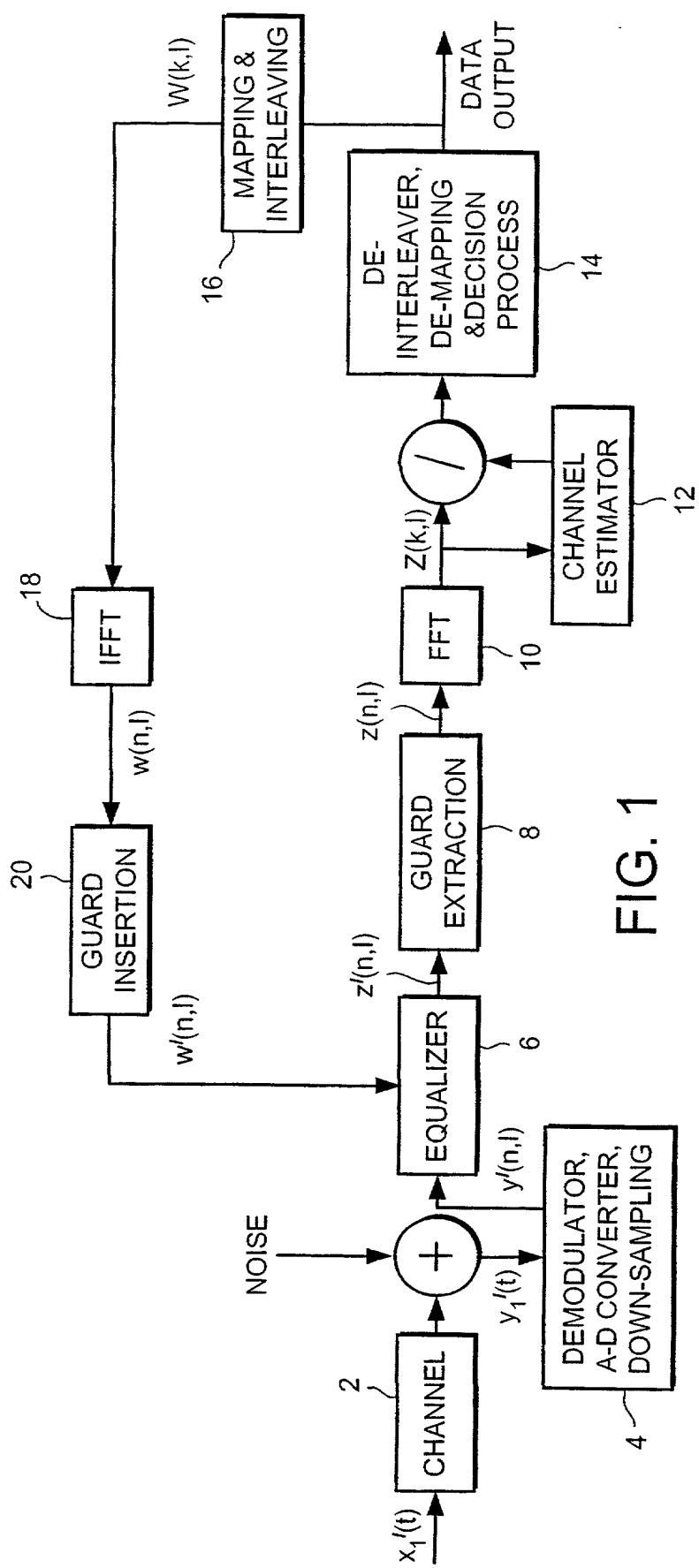


FIG. 1

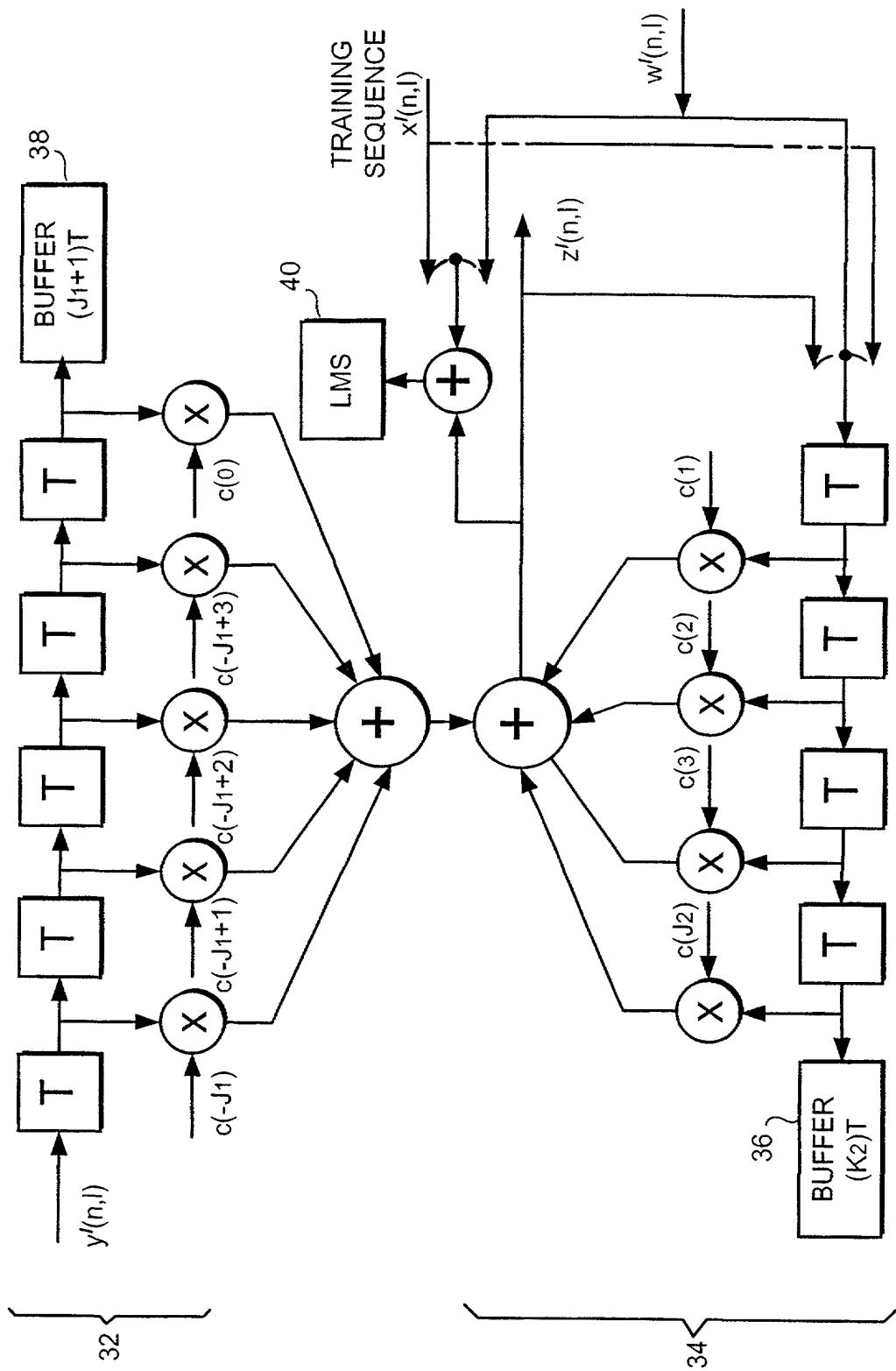
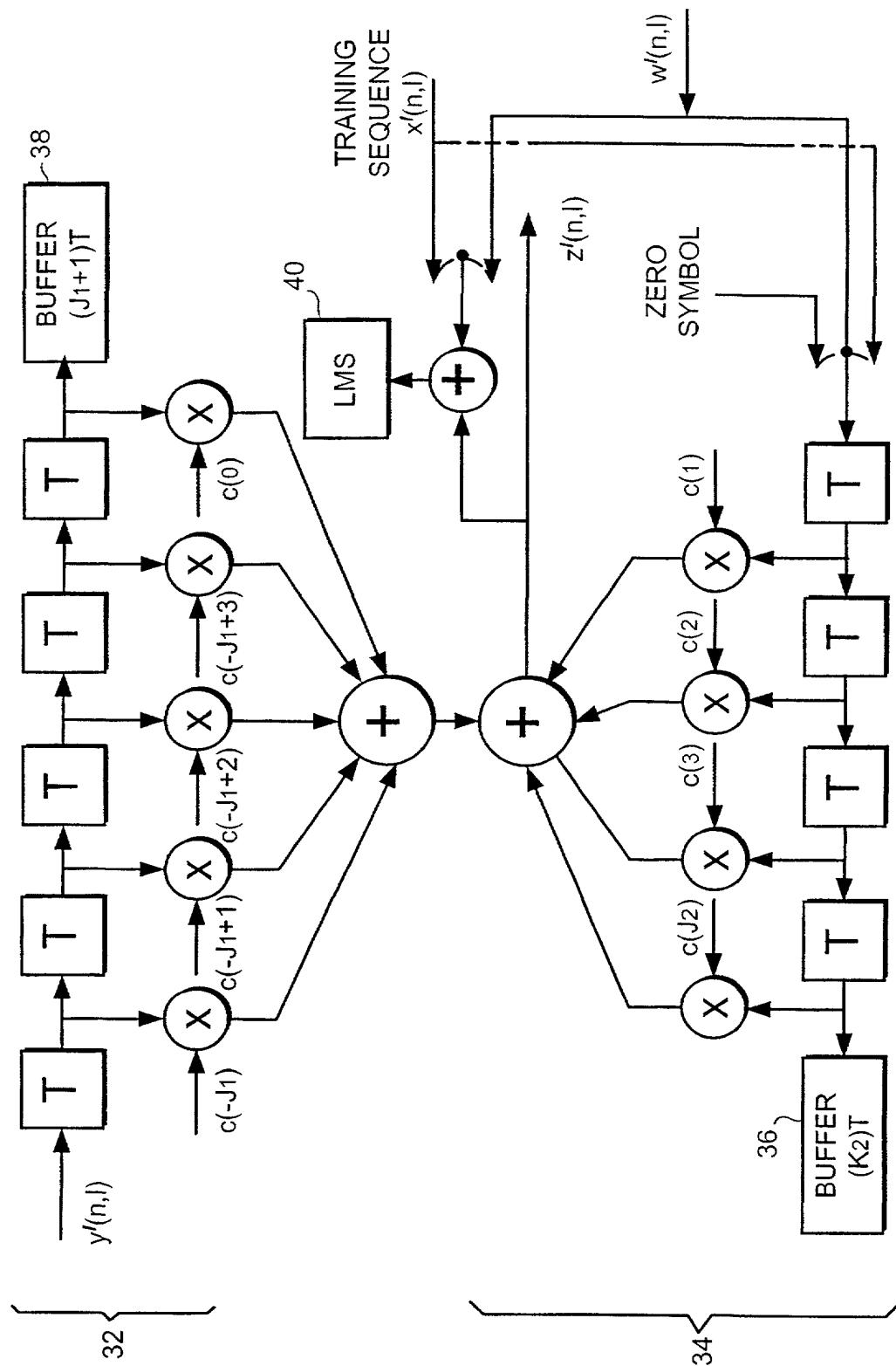


FIG. 2



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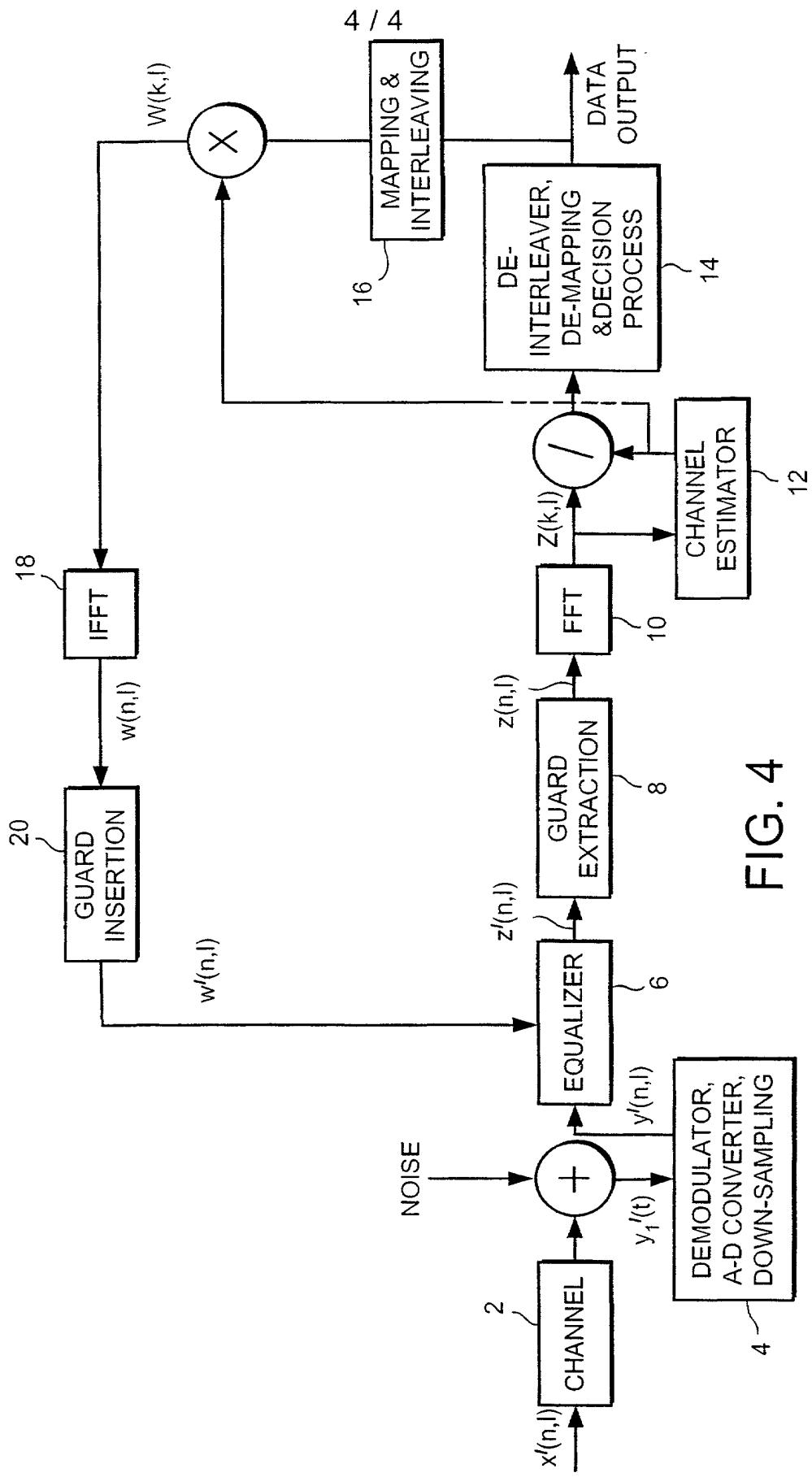


FIG. 4

## DECLARATION

As a below named inventor, I declare that:

My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: OFDM RECEIVER WITH ADAPTIVE EQUALISER the specification of which was filed on 16 August 2001 as Application No. 09/913,840 and was amended on \_\_\_\_\_ (if applicable).

I have reviewed and understand the contents of the above identified specification, including the claims, as amended by an amendment referred to above. I acknowledge the duty to disclose information which is material to patentability as defined in Title 37 Code of Federal Regulations, Section 1.56. I claim foreign priority benefits under Title 35, United States Code, Section 119 of an foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed.

### Prior Foreign Application(s)

Country	Application No.	Date of Filing	Priority Claimed Under 35 USC 119
Great Britain	9901491.2	22 January 1999	Yes
PCT	PCT/GB00/00174	24 January 2000	Yes

I claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, Section 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Application No.	Date of Filing	Status

Full Name of Inventor 1:	Last Name: <u>ARMOUR</u>	First Name: <u>Simon</u>	Middle Name or Initial:	
Residence & Citizenship:	City: <u>Bath</u>	State/Foreign Country: <u>GBX</u>	Country of Citizenship: <u>Great Britain</u>	
Post Office Address:	Post Office Address: <u>689 Wellsway Odd Down</u>	City: <u>Bath</u>	State/Country: <u>Great Britain</u>	Postal Code: <u>BA2 2TZ</u>
Full Name of Inventor 2:	Last Name: <u>BULL</u>	First Name: <u>David</u>	Middle Name or Initial: <u>Roger</u>	
Residence & Citizenship:	City: <u>Nr. Chepstow</u>	State/Foreign Country: <u>GBX</u>	Country of Citizenship: <u>Great Britain</u>	
Post Office Address:	Post Office Address: <u>Netherways</u> <u>Netherhohpe Land</u> <u>Tidenham</u>	City: <u>Nr. Chepstow</u>	State/Country: <u>Great Britain</u>	Postal Code: <u>Monmouthshire</u> <u>NP16 7JE</u>

Full Name of Inventor 32: <u>NIX</u>	Last Name: <u>NIX</u>	First Name: <u>Andrew</u>	Middle Name or Initial: <u>Robert</u>	
Residence & Citizenship: <u>Bristol</u>	City: <u>Bristol</u> <i>GBX</i>	State/Foreign Country: <u>Great Britain</u>	Country of Citizenship: <u>Great Britain</u>	
Post Office Address: <u>7 Bellview</u> <u>Clifton</u>	Post Office Address: <u>7 Bellview</u> <u>Clifton</u>	City: <u>Bristol</u>	State/Country: <u>Great Britain</u>	Postal Code: <u>BS8 1DA</u>

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Inventor one Simon ARMOUR Date: 18/12/01

Signature:  Citizen of: Great Britain

Inventor two David Roger BULL Date: 17/12/01

Signature:  Citizen of: Great Britain

Inventor three Andrew Robert NIX Date: 17/12/01

Signature:  Citizen of: Great Britain